

[MULTI-MEMORY ARCHITECTURE AND ACCESS CONTROLLER THEREFOR]

Abstract of Disclosure

A multi-memory architecture and a memory access controller therefor are proposed. The multi-memory architecture is composed of at least two different types of memory devices and is used to provide a specific externally-accessible data storage capacity. The multi-memory architecture comprises a first memory device and a second memory device; wherein the first memory device has a first data storage capacity; and the second memory device has a second data storage capacity. The pin configuration of the multi-memory architecture is compatible with the first memory device with the externally accessible data storage capacity, wherein the externally-accessible data storage capacity can be either the first data storage capacity, the second data storage capacity, or the sum of the first and second data storage capacities.

Figures